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				Application Number	10/632224
				Filing Date	7/31/2003
				First Named Inventor	Thomas C. McDonald
				Group Art Unit	2181
				Examiner Name	Jesse R. Moll
Sheet	1	of	1	Attorney Docket Number	CNTR.2141

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
JM		6101595	08-08-2000	Pickett et al.	
JM		6157988	12-05-2000	Eric M. Dowling	
JM		6374350	04-16-2002	D'Sa et al	
JM		5434985	07-18-1995	Emma et al	
JM		5553246	09-03-1996	Hiromi Suzuki	
JM		6122729	09-19-2000	Thang M. Tran	
JM		6748441	06-08-2004	David James Gemmell	
JM		6321321	11-20-2001	Mark W. Johnson	

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ²
JM		Online Computing Dictionary. http://instantweb.com/d/dictionary/foldoc.cgi?query=btb 05/05/1995. Branch Target Buffer		
JM		Patterson et al. "Computer Organization & Design: The Hardware/Software Interface." Morgan Kaufmann Publishers, Inc. San Francisco, CA. 1998 Page 469		
JM		Jimenez et al. "The Impact of Delay on the Design of Branch Predictors." 2000		
JM		EBERLY et al. "The Correlation Branch Target Address Cache" May 1996.		
JM		Patterson et al. "Computer Organization & Design: The Hardware/Software Interface." Morgan Kaufmann Publishers, Inc. San Francisco, CA. 1998 Page 453-455		

Examiner Signature	/Jesse Moll/	Date Considered	06/09/2006
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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